

# NEXT PHASE FOR OVER \$5 BILLION IN CHIPS R&D INVESTMENTS, INCLUDING THE NATIONAL SEMICONDUCTOR TECHNOLOGY CENTER (NSTC)

Leaders from the Departments of Commerce, Defense, and Energy; and the National Science Foundation; and the Chief Executive Officer of the National Center for the Advancement of Semiconductor Technology (Natcast) gathered at the White House on February 9 to announce over \$5 billion in expected investment in the CHIPS R&D program, including the National Semiconductor Technology Center (NSTC), and formally establish a public-private consortium for the NSTC. The announcement included hundreds of millions of dollars of expected investment in the semiconductor workforce; along with specific funding announcements in packaging, metrology, and a CHIPS Manufacturing USA Institute.

The NSTC is the centerpiece of CHIPS for America's \$11 billion research and development (R&D) program. A once in-a-generation opportunity, the NSTC will bring together government, industry, labor, customers, suppliers, educational institutions, entrepreneurs, and investors to accelerate the pace of new innovations from idea to marketplace. As a public-private consortium, the NSTC will lower the barriers to participation in semiconductor R&D to create a more vibrant national ecosystem and to directly address fundamental needs for a skilled, diverse semiconductor workforce.

Following the announcements and consortium agreement signing, leaders from across the semiconductor community participated in roundtables led by White House Office of Science and Technology Policy Director Arati Prabhakar, National Science Foundation Director Sethuraman Panchanathan, and Under Secretary of Commerce for Standards and Technology and National Institute of Standards and Technology (NIST) Director Laurie E. Locascio, along with senior government leaders from the White House; Departments of Commerce, Defense, Energy, Labor, Education; and the National Science Foundation. The roundtables centered on the R&D opportunities and need for industry

participation, along with how CHIPS for America will help increase access to talent for employers and access to opportunity for Americans who want to work in the semiconductor industry.

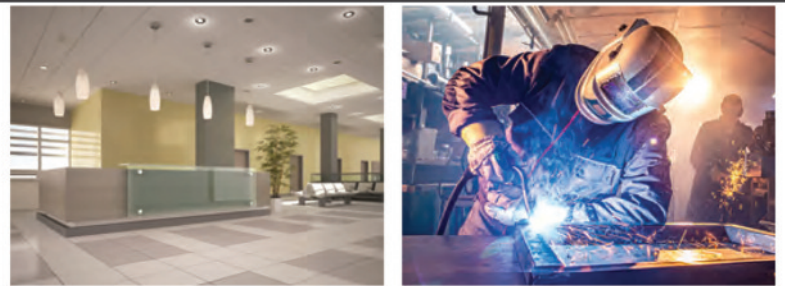


"CHIPS research and development programs are at the core of our greatest innovations and help to find the solutions for the semiconductor industry's most pressing challenges. With strategic investments in R&D complementing targeted industry incentives, CHIPS for America will not only bring semiconductor manufacturing back to the U.S. – it will keep it here for good. As we create opportunities for good-paying jobs, the workforce initiatives, such as the NSTC Workforce Center of Excellence, will help ensure a diverse, skilled, and prepared workforce across the nation," said Secretary of Commerce Gina Raimondo.

The CHIPS and Science Act appropriated \$39 billion to the Department of Commerce in funding to onshore semiconductor manufacturing through an incentives program. The appropriation also included \$11 billion to advance U.S. leadership in semiconductor R&D through four programs: the NSTC, the National Advanced Packaging Manufacturing Program, the CHIPS Metrology Program and the CHIPS Manufacturing USA Institute.

With the official launch of the NSTC consortium, there are opportunities to express interest in joining the NSTC. Further information about this may be found at [www.Natcast.org](http://www.Natcast.org).

The NSTC is one of four CHIPS for America research and development programs. Together, these programs are establishing the innovation ecosystem needed to ensure that American semiconductor fabrication facilities, including those funded by the CHIPS for America Act, produce the world's most sophisticated and advanced technologies. CHIPS for America's Jay Lewis, NSTC Program Director, and Deirdre Hanford, CEO of Natcast, will provide an update on early NSTC activities in March 2024.



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